Abstract

Input data segments of received symbols are continuously stored in a decision feedback equalizer buffer at a symbol rate S. Output data sections of received symbols are supplied from the decision feedback equalizer buffer at an output rate of nS such that void times separate the output data sections, and n > 1. The received symbols supplied by the decision feedback equalizer buffer are equalized in a decision feedback equalizer to provide equalized symbols;, and the equalized symbols are decoded by a decoder to provide decoded symbols. Adjustments for the decision feedback equalizer are calculated during the void times such that the adjustments are calculated based on both the received symbols supplied by the decision feedback equalizer buffer and the decoded symbols. The adjustments are applied to the decision feedback equalizer.